## 6ES7214-1AD23-0XB0

\*\*\*Spare part\*\*\* SIMATIC S7-200, CPU 224 Compact unit, DC power supply 14 DI DC/10 DO DC, 8/12 KB progr./8 KB data, PROFIBUS DP

**Data sheet** 



Figure similar

Supply voltage

Rated value (DC) • 24 V DC Yes Load voltage L+ • Rated value (DC) 24 V • permissible range, lower limit (DC) 20.4 V • permissible range, upper limit (DC) 28.8 V Input current Inrush current, max. 12 A; at 28.8 V from supply voltage L+, max. 700 mA; 110 mA to 700 mA, output current for expansion modules (5 V DC) 660 mA **Encoder supply** 24 V encoder supply Yes; permissible range: 15.4 to 28.8 V • 24 V • Short-circuit protection Yes; electronic at 280 mA 280 mA • Output current, max. **Power loss** Power loss, typ. 7 W Number of memory modules (optional) 1; pluggable memory module, content identical with integral EEPROM; can additionally store recipes, data logs and other files Work memory • integrated (for program) 12 kbyte; 8 KB with active run-time edit • integrated (for data) 8 kbyte Backup Yes; Program: Entire program maintenance-free on integral EEPROM, present programmable via CPU; data: Entire DB 1 loaded from PG/PC maintenance-free on integral EEPROM, current values of DB 1 in RAM, retentive memory bits, timers, counters, etc. maintenance-free via highperformance capacitor; optional battery for long-term buffering **Battery** Backup battery • Backup time, max. 100 h; (min. 70 h at 40 °C); 200 days (typ.) with optional battery module

expandable

Retentivity

CPU processing times for bit operations, max.

S7 counter Number

Counters, timers and their retentivity

 $0.22 \, \mu s$ 

256

P 4 1 1	V
— adjustable	Yes; via high-performance capacitor or battery
— lower limit	1
— upper limit	256
Counting range	0
— lower limit — upper limit	32 767
S7 times	32 101
Number	256
Retentivity	230
— adjustable	Yes; via high-performance capacitor or battery
— upper limit	64
Time range	
— lower limit	1 ms
— upper limit	54 min; 4 timers: 1 ms to 30 s; 16 timers: 10 ms to 5 min; 236 timers: 100 ms to 54 min
Data areas and their retentivity	
Flag	
Size, max.	32 byte
Retentivity available	Yes; M 0.0 to M 31.7
<ul> <li>of which retentive with battery</li> </ul>	0 to 255, via high-performance capacitor or battery, adjustable
<ul> <li>of which retentive without battery</li> </ul>	0 to 112 in EEPROM, adjustable
Hardware configuration	
Number of expansion units, max.	7; Only expansion modules of the S7-22x series can be used. Due to the limited output current, the use of expansion modules may be limited.
connectable programming devices/PCs	SIMATIC PG/PC, standard PC
Expansion modules	
Analog inputs/outputs, max.	35; max. 28 inputs and 7 outputs (EM) or max. 0 inputs and 14 outputs (EM)
<ul> <li>Digital inputs/outputs, max.</li> </ul>	168; max. 94 inputs and 74 outputs (CPU + EM)
<ul> <li>AS-Interface inputs/outputs, max.</li> </ul>	62; AS-Interface A/B slaves (CP 243-2)
Digital inputs	
Digital Iliputo	
Number of digital inputs	14
	14 Yes; optionally, per group
Number of digital inputs	
Number of digital inputs  Source/sink input  Input voltage  • Rated value (DC)	Yes; optionally, per group  24 V
Number of digital inputs  Source/sink input  Input voltage  Rated value (DC)  for signal "0"	Yes; optionally, per group
Number of digital inputs  Source/sink input  Input voltage  Rated value (DC)  for signal "0"  for signal "1"	Yes; optionally, per group  24 V
Number of digital inputs  Source/sink input  Input voltage  • Rated value (DC)  • for signal "0"  • for signal "1"  Input current	Yes; optionally, per group  24 V 0 to 5 V min. 15 V
Number of digital inputs  Source/sink input  Input voltage  • Rated value (DC)  • for signal "0"  • for signal "1"  Input current  • for signal "1", typ.	Yes; optionally, per group  24 V 0 to 5 V
Number of digital inputs  Source/sink input  Input voltage  Rated value (DC)  for signal "0"  for signal "1"  Input current  for signal "1", typ.  Input delay (for rated value of input voltage)	Yes; optionally, per group  24 V 0 to 5 V min. 15 V
Number of digital inputs  Source/sink input  Input voltage  Rated value (DC)  for signal "0"  for signal "1"  Input current  for signal "1", typ.  Input delay (for rated value of input voltage)  for standard inputs	Yes; optionally, per group  24 V 0 to 5 V min. 15 V  2.5 mA
Number of digital inputs  Source/sink input  Input voltage  Rated value (DC)  for signal "0"  for signal "1"  Input current  for signal "1", typ.  Input delay (for rated value of input voltage)  for standard inputs  parameterizable	Yes; optionally, per group  24 V 0 to 5 V min. 15 V  2.5 mA  Yes; all
Number of digital inputs  Source/sink input  Input voltage  Rated value (DC)  for signal "0"  for signal "1"  Input current  for signal "1", typ.  Input delay (for rated value of input voltage)  for standard inputs  parameterizable  at "0" to "1", min.	Yes; optionally, per group  24 V 0 to 5 V min. 15 V  2.5 mA  Yes; all 0.2 ms
Number of digital inputs  Source/sink input  Input voltage  Rated value (DC)  for signal "0"  for signal "1"  Input current  for signal "1", typ.  Input delay (for rated value of input voltage)  for standard inputs  parameterizable  at "0" to "1", min.  at "0" to "1", max.	Yes; optionally, per group  24 V 0 to 5 V min. 15 V  2.5 mA  Yes; all
Number of digital inputs  Source/sink input  Input voltage  Rated value (DC)  for signal "0"  for signal "1"  Input current  for signal "1", typ.  Input delay (for rated value of input voltage)  for standard inputs  parameterizable  at "0" to "1", min.  at "0" to "1", max.  for interrupt inputs	Yes; optionally, per group  24 V 0 to 5 V min. 15 V  2.5 mA  Yes; all 0.2 ms 12.8 ms
Number of digital inputs  Source/sink input  Input voltage  Rated value (DC)  for signal "0"  for signal "1"  Input current  for signal "1", typ.  Input delay (for rated value of input voltage)  for standard inputs  parameterizable  at "0" to "1", min.  at "0" to "1", max.  for interrupt inputs  parameterizable  parameterizable  parameterizable	Yes; optionally, per group  24 V 0 to 5 V min. 15 V  2.5 mA  Yes; all 0.2 ms
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Number of digital inputs  Source/sink input  Input voltage  Rated value (DC)  for signal "0"  for signal "1"  Input current  for signal "1", typ.  Input delay (for rated value of input voltage)  for standard inputs  parameterizable  at "0" to "1", min.  at "0" to "1", max.  for interrupt inputs  parameterizable  for technological functions  parameterizable	Yes; optionally, per group  24 V 0 to 5 V min. 15 V  2.5 mA  Yes; all 0.2 ms 12.8 ms
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Number of digital inputs  Source/sink input  Input voltage  Rated value (DC)  for signal "0"  for signal "1"  Input current  for signal "1", typ.  Input delay (for rated value of input voltage)  for standard inputs  parameterizable  at "0" to "1", min.  at "0" to "1", max.  for interrupt inputs  parameterizable  for technological functions  parameterizable  Cable length  shielded, max.	Yes; optionally, per group  24 V 0 to 5 V min. 15 V  2.5 mA  Yes; all 0.2 ms 12.8 ms  Yes; I 0.0 to I 0.3  Yes; (E 0.0 to E 1.5) 30 kHz  500 m; Standard input: 500 m, high-speed counters: 50 m
Number of digital inputs  Source/sink input  Input voltage  Rated value (DC)  for signal "0"  for signal "1"  Input current  for signal "1", typ.  Input delay (for rated value of input voltage)  for standard inputs  parameterizable  at "0" to "1", min.  at "0" to "1", max.  for interrupt inputs  parameterizable  for technological functions  parameterizable  Cable length  shielded, max.  unshielded, max.	Yes; optionally, per group  24 V 0 to 5 V min. 15 V  2.5 mA  Yes; all 0.2 ms 12.8 ms  Yes; I 0.0 to I 0.3  Yes; (E 0.0 to E 1.5) 30 kHz
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Number of digital inputs  Source/sink input  Input voltage  Rated value (DC)  for signal "0"  for signal "1"  Input current  for signal "1", typ.  Input delay (for rated value of input voltage)  for standard inputs  parameterizable  at "0" to "1", min.  at "0" to "1", max.  for interrupt inputs  parameterizable  for technological functions  parameterizable  Cable length  shielded, max.  unshielded, max.  Jugital outputs  Number of digital outputs  Short-circuit protection  Limitation of inductive shutdown voltage to	Yes; optionally, per group  24 V 0 to 5 V min. 15 V  2.5 mA  Yes; all 0.2 ms 12.8 ms  Yes; I 0.0 to I 0.3  Yes; (E 0.0 to E 1.5) 30 kHz  500 m; Standard input: 500 m, high-speed counters: 50 m 300 m; not for high-speed signals  10; Transistor No; to be provided externally
Number of digital inputs  Source/sink input  Input voltage  Rated value (DC)  for signal "0"  for signal "1"  Input current  for signal "1", typ.  Input delay (for rated value of input voltage)  for standard inputs  parameterizable  at "0" to "1", min.  at "0" to "1", max.  for interrupt inputs  parameterizable  for technological functions  parameterizable  Cable length  shielded, max.  unshielded, max.  Digital outputs  Number of digital outputs  Short-circuit protection	Yes; optionally, per group  24 V 0 to 5 V min. 15 V  2.5 mA  Yes; all 0.2 ms 12.8 ms  Yes; I 0.0 to I 0.3  Yes; (E 0.0 to E 1.5) 30 kHz  500 m; Standard input: 500 m, high-speed counters: 50 m 300 m; not for high-speed signals  10; Transistor No; to be provided externally
Number of digital inputs  Source/sink input  Input voltage  Rated value (DC)  for signal "0"  for signal "1"  Input current  for signal "1", typ.  Input delay (for rated value of input voltage)  for standard inputs  parameterizable  at "0" to "1", min.  at "0" to "1", max.  for interrupt inputs  parameterizable  for technological functions  parameterizable  Cable length  shielded, max.  unshielded, max.  unshielded, max.  Sigital outputs  Number of digital outputs  Short-circuit protection  Limitation of inductive shutdown voltage to  Switching capacity of the outputs	Yes; optionally, per group  24 V 0 to 5 V min. 15 V  2.5 mA  Yes; all 0.2 ms 12.8 ms  Yes; I 0.0 to I 0.3  Yes; (E 0.0 to E 1.5) 30 kHz  500 m; Standard input: 500 m, high-speed counters: 50 m 300 m; not for high-speed signals  10; Transistor No; to be provided externally 1 W
Number of digital inputs  Source/sink input  Input voltage  Rated value (DC)  for signal "0"  for signal "1"  Input current  for signal "1", typ.  Input delay (for rated value of input voltage)  for standard inputs  parameterizable  at "0" to "1", min.  at "0" to "1", max.  for interrupt inputs  parameterizable  for technological functions  parameterizable  Cable length  shielded, max.  unshielded, max.  Unigital outputs  Number of digital outputs  Short-circuit protection  Limitation of inductive shutdown voltage to  Switching capacity of the outputs  with resistive load, max.	Yes; optionally, per group  24 V 0 to 5 V min. 15 V  2.5 mA  Yes; all 0.2 ms 12.8 ms  Yes; I 0.0 to I 0.3  Yes; (E 0.0 to E 1.5) 30 kHz  500 m; Standard input: 500 m, high-speed counters: 50 m 300 m; not for high-speed signals  10; Transistor No; to be provided externally 1 W  0.75 A
Number of digital inputs  Source/sink input  Input voltage  Rated value (DC)  for signal "0"  for signal "1"  Input current  for signal "1", typ.  Input delay (for rated value of input voltage)  for standard inputs  parameterizable  at "0" to "1", min.  at "0" to "1", max.  for interrupt inputs  parameterizable  for technological functions  parameterizable  Cable length  shielded, max.  unshielded, max.  unshielded, max.  bigital outputs  Number of digital outputs  Short-circuit protection  Limitation of inductive shutdown voltage to  Switching capacity of the outputs  with resistive load, max.  on lamp load, max.	Yes; optionally, per group  24 V 0 to 5 V min. 15 V  2.5 mA  Yes; all 0.2 ms 12.8 ms  Yes; I 0.0 to I 0.3  Yes; (E 0.0 to E 1.5) 30 kHz  500 m; Standard input: 500 m, high-speed counters: 50 m 300 m; not for high-speed signals  10; Transistor No; to be provided externally 1 W  0.75 A

Output current	
• for signal "1" rated value	750 mA
• for signal "0" residual current, max.	10 μΑ
Output delay with resistive load	
• "0" to "1", max.	15 $\mu$ s; of the standard outputs, max. (Q 0.2 to Q 1.1) 2 $\mu$ s; of the pulse outputs, max. (Q 0.0 to Q 0.1) 2 $\mu$ s
• "1" to "0", max.	130 $\mu$ s; of the standard outputs, max. (Q 0.2 to Q 1.1) 10 $\mu$ s; of the pulse outputs, max. (Q 0.0 to Q 0.1) 10 $\mu$ s
Parallel switching of two outputs	
for uprating	Yes
Switching frequency	
of the pulse outputs, with resistive load, max.	20 kHz; Q0.0 to Q0.1
Total current of the outputs (per group)	
all mounting positions	
— up to 40 °C, max.	6 A
horizontal installation	
— up to 55 °C, max.	6 A
Relay outputs	
Number of relay outputs	0
Cable length	
• shielded, max.	500 m
• unshielded, max.	150 m
Analog inputs	
Number of analog potentiometers	2; Analog potentiometer; resolution 8 bit
Encoder	
Connectable encoders	
• 2-wire sensor	Yes
<ul> <li>permissible quiescent current (2-wire sensor), max.</li> </ul>	1 mA
4 Interfece	
1. Interface	
	Integrated RS 485 interface
Interface type Protocols	Integrated RS 485 interface
Interface type	Integrated RS 485 interface  Yes; As MPI slave for data exchange with MPI masters (S7-300/S7-400 CPUs, OPs, TDs, Push Button Panels); S7-200-internal CPU/CPU communication is possible in the MPI network with restrictions; transmission rates: 19.2/187.5 kbit/s
Interface type Protocols	Yes; As MPI slave for data exchange with MPI masters (S7-300/S7-400 CPUs, OPs, TDs, Push Button Panels); S7-200-internal CPU/CPU communication is possible in the MPI network with restrictions;
Interface type Protocols  • MPI	Yes; As MPI slave for data exchange with MPI masters (S7-300/S7-400 CPUs, OPs, TDs, Push Button Panels); S7-200-internal CPU/CPU communication is possible in the MPI network with restrictions; transmission rates: 19.2/187.5 kbit/s  Yes; with PPI protocol for program functions, HMI functions (TD 200, OP), S7-200-internal CPU/CPU communication; transmission rates
Interface type Protocols  • MPI  • PPI	Yes; As MPI slave for data exchange with MPI masters (S7-300/S7-400 CPUs, OPs, TDs, Push Button Panels); S7-200-internal CPU/CPU communication is possible in the MPI network with restrictions; transmission rates: 19.2/187.5 kbit/s  Yes; with PPI protocol for program functions, HMI functions (TD 200, OP), S7-200-internal CPU/CPU communication; transmission rates 9.6/19.2/187.5 kbit/s  Yes; As freely programmable interface with interrupt facility for serial data exchange with third-party devices with ASCII protocol transfer rates: 1.2 / 2.4 / 4.8 / 9.6 / 19.2 / 38.4 / 57.6 / 115.2 kbps; the PC/PPI
Interface type Protocols  • MPI  • PPI  • serial data exchange	Yes; As MPI slave for data exchange with MPI masters (S7-300/S7-400 CPUs, OPs, TDs, Push Button Panels); S7-200-internal CPU/CPU communication is possible in the MPI network with restrictions; transmission rates: 19.2/187.5 kbit/s  Yes; with PPI protocol for program functions, HMI functions (TD 200, OP), S7-200-internal CPU/CPU communication; transmission rates 9.6/19.2/187.5 kbit/s  Yes; As freely programmable interface with interrupt facility for serial data exchange with third-party devices with ASCII protocol transfer rates: 1.2 / 2.4 / 4.8 / 9.6 / 19.2 / 38.4 / 57.6 / 115.2 kbps; the PC/PPI
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Interface type  Protocols  • MPI  • PPI  • serial data exchange  MPI  • Transmission rate, min.	Yes; As MPI slave for data exchange with MPI masters (S7-300/S7-400 CPUs, OPs, TDs, Push Button Panels); S7-200-internal CPU/CPU communication is possible in the MPI network with restrictions; transmission rates: 19.2/187.5 kbit/s  Yes; with PPI protocol for program functions, HMI functions (TD 200, OP), S7-200-internal CPU/CPU communication; transmission rates 9.6/19.2/187.5 kbit/s  Yes; As freely programmable interface with interrupt facility for serial data exchange with third-party devices with ASCII protocol transfer rates: 1.2 / 2.4 / 4.8 / 9.6 / 19.2 / 38.4 / 57.6 / 115.2 kbps; the PC/PPI cable can also be used as RS 232/RS 485 converter
Interface type  Protocols  • MPI  • PPI  • serial data exchange  MPI  • Transmission rate, min.  • Transmission rate, max.	Yes; As MPI slave for data exchange with MPI masters (S7-300/S7-400 CPUs, OPs, TDs, Push Button Panels); S7-200-internal CPU/CPU communication is possible in the MPI network with restrictions; transmission rates: 19.2/187.5 kbit/s  Yes; with PPI protocol for program functions, HMI functions (TD 200, OP), S7-200-internal CPU/CPU communication; transmission rates 9.6/19.2/187.5 kbit/s  Yes; As freely programmable interface with interrupt facility for serial data exchange with third-party devices with ASCII protocol transfer rates: 1.2 / 2.4 / 4.8 / 9.6 / 19.2 / 38.4 / 57.6 / 115.2 kbps; the PC/PPI cable can also be used as RS 232/RS 485 converter
Interface type  Protocols  • MPI  • PPI  • serial data exchange  MPI  • Transmission rate, min.  • Transmission rate, max.  Integrated Functions	Yes; As MPI slave for data exchange with MPI masters (S7-300/S7-400 CPUs, OPs, TDs, Push Button Panels); S7-200-internal CPU/CPU communication is possible in the MPI network with restrictions; transmission rates: 19.2/187.5 kbit/s  Yes; with PPI protocol for program functions, HMI functions (TD 200, OP), S7-200-internal CPU/CPU communication; transmission rates 9.6/19.2/187.5 kbit/s  Yes; As freely programmable interface with interrupt facility for serial data exchange with third-party devices with ASCII protocol transfer rates: 1.2 / 2.4 / 4.8 / 9.6 / 19.2 / 38.4 / 57.6 / 115.2 kbps; the PC/PPI cable can also be used as RS 232/RS 485 converter
Interface type  Protocols  • MPI  • PPI  • serial data exchange  MPI  • Transmission rate, min.  • Transmission rate, max.  Integrated Functions  Counter	Yes; As MPI slave for data exchange with MPI masters (S7-300/S7-400 CPUs, OPs, TDs, Push Button Panels); S7-200-internal CPU/CPU communication is possible in the MPI network with restrictions; transmission rates: 19.2/187.5 kbit/s  Yes; with PPI protocol for program functions, HMI functions (TD 200, OP), S7-200-internal CPU/CPU communication; transmission rates 9.6/19.2/187.5 kbit/s  Yes; As freely programmable interface with interrupt facility for serial data exchange with third-party devices with ASCII protocol transfer rates: 1.2 / 2.4 / 4.8 / 9.6 / 19.2 / 38.4 / 57.6 / 115.2 kbps; the PC/PPI cable can also be used as RS 232/RS 485 converter  19.2 kbit/s  187.5 kbit/s  6; High-speed counters (30 kHz each), 32 bit (incl. sign), can be used as up/down counters or for connecting 2 incremental encoders with 2 pulse trains offset by 90° (max. 20 kHz (A/B counters)); parameterizable enable and reset input; interrupt facilities (incl. call of subroutine with any content) when the setpoint is reached; reversal in counting
Interface type Protocols  • MPI  • PPI  • serial data exchange  MPI  • Transmission rate, min. • Transmission rate, max. Integrated Functions  Counter • Number of counters	Yes; As MPI slave for data exchange with MPI masters (S7-300/S7-400 CPUs, OPs, TDs, Push Button Panels); S7-200-internal CPU/CPU communication is possible in the MPI network with restrictions; transmission rates: 19.2/187.5 kbit/s  Yes; with PPI protocol for program functions, HMI functions (TD 200, OP), S7-200-internal CPU/CPU communication; transmission rates 9.6/19.2/187.5 kbit/s  Yes; As freely programmable interface with interrupt facility for serial data exchange with third-party devices with ASCII protocol transfer rates: 1.2 / 2.4 / 4.8 / 9.6 / 19.2 / 38.4 / 57.6 / 115.2 kbps; the PC/PPI cable can also be used as RS 232/RS 485 converter  19.2 kbit/s  187.5 kbit/s  6; High-speed counters (30 kHz each), 32 bit (incl. sign), can be used as up/down counters or for connecting 2 incremental encoders with 2 pulse trains offset by 90° (max. 20 kHz (A/B counters)); parameterizable enable and reset input; interrupt facilities (incl. call of subroutine with any content) when the setpoint is reached; reversal in counting direction, etc.
Interface type Protocols  • MPI  • PPI  • serial data exchange  MPI  • Transmission rate, min. • Transmission rate, max.  Integrated Functions  Counter  • Number of counters  • Counting frequency, max.	Yes; As MPI slave for data exchange with MPI masters (S7-300/S7-400 CPUs, OPs, TDs, Push Button Panels); S7-200-internal CPU/CPU communication is possible in the MPI network with restrictions; transmission rates: 19.2/187.5 kbit/s  Yes; with PPI protocol for program functions, HMI functions (TD 200, OP), S7-200-internal CPU/CPU communication; transmission rates 9.6/19.2/187.5 kbit/s  Yes; As freely programmable interface with interrupt facility for serial data exchange with third-party devices with ASCII protocol transfer rates: 1.2 / 2.4 / 4.8 / 9.6 / 19.2 / 38.4 / 57.6 / 115.2 kbps; the PC/PPI cable can also be used as RS 232/RS 485 converter  19.2 kbit/s  19.2 kbit/s  187.5 kbit/s  6; High-speed counters (30 kHz each), 32 bit (incl. sign), can be used as up/down counters or for connecting 2 incremental encoders with 2 pulse trains offset by 90° (max. 20 kHz (A/B counters)); parameterizable enable and reset input; interrupt facilities (incl. call of subroutine with any content) when the setpoint is reached; reversal in counting direction, etc. 30 kHz
Interface type Protocols  • MPI  • PPI  • serial data exchange  MPI  • Transmission rate, min. • Transmission rate, max.  Integrated Functions  Counter  • Number of counters  • Counting frequency, max.  Number of alarm inputs	Yes; As MPI slave for data exchange with MPI masters (S7-300/S7-400 CPUs, OPs, TDs, Push Button Panels); S7-200-internal CPU/CPU communication is possible in the MPI network with restrictions; transmission rates: 19.2/187.5 kbit/s  Yes; with PPI protocol for program functions, HMI functions (TD 200, OP), S7-200-internal CPU/CPU communication; transmission rates 9.6/19.2/187.5 kbit/s  Yes; As freely programmable interface with interrupt facility for serial data exchange with third-party devices with ASCII protocol transfer rates: 1.2 / 2.4 / 4.8 / 9.6 / 19.2 / 38.4 / 57.6 / 115.2 kbps; the PC/PPI cable can also be used as RS 232/RS 485 converter  19.2 kbit/s  187.5 kbit/s  6; High-speed counters (30 kHz each), 32 bit (incl. sign), can be used as up/down counters or for connecting 2 incremental encoders with 2 pulse trains offset by 90° (max. 20 kHz (A/B counters)); parameterizable enable and reset input; interrupt facilities (incl. call of subroutine with any content) when the setpoint is reached; reversal in counting direction, etc.  30 kHz  4; 4 rising edges and/or 4 falling edges  2; High-speed outputs, 20 kHz, with interrupt option; pulse-width and
Interface type Protocols  MPI  PPI  Serial data exchange  MPI  Transmission rate, min. Transmission rate, max. Integrated Functions  Counter Number of counters  Counting frequency, max.  Number of alarm inputs  Number of pulse outputs	Yes; As MPI slave for data exchange with MPI masters (S7-300/S7-400 CPUs, OPs, TDs, Push Button Panels); S7-200-internal CPU/CPU communication is possible in the MPI network with restrictions; transmission rates: 19.2/187.5 kbit/s  Yes; with PPI protocol for program functions, HMI functions (TD 200, OP), S7-200-internal CPU/CPU communication; transmission rates 9.6/19.2/187.5 kbit/s  Yes; As freely programmable interface with interrupt facility for serial data exchange with third-party devices with ASCII protocol transfer rates: 1.2 / 2.4 / 4.8 / 9.6 / 19.2 / 38.4 / 57.6 / 115.2 kbps; the PC/PPI cable can also be used as RS 232/RS 485 converter  19.2 kbit/s  19.2 kbit/s  187.5 kbit/s  6; High-speed counters (30 kHz each), 32 bit (incl. sign), can be used as up/down counters or for connecting 2 incremental encoders with 2 pulse trains offset by 90° (max. 20 kHz (A/B counters)); parameterizable enable and reset input; interrupt facilities (incl. call of subroutine with any content) when the setpoint is reached; reversal in counting direction, etc.  30 kHz  4; 4 rising edges and/or 4 falling edges  2; High-speed outputs, 20 kHz, with interrupt option; pulse-width and frequency modulation option
Interface type Protocols  • MPI  • PPI  • serial data exchange  MPI  • Transmission rate, min. • Transmission rate, max.  Integrated Functions  Counter  • Number of counters  • Counting frequency, max.  Number of alarm inputs  Number of pulse outputs  Limit frequency (pulse)  Potential separation	Yes; As MPI slave for data exchange with MPI masters (S7-300/S7-400 CPUs, OPs, TDs, Push Button Panels); S7-200-internal CPU/CPU communication is possible in the MPI network with restrictions; transmission rates: 19.2/187.5 kbit/s  Yes; with PPI protocol for program functions, HMI functions (TD 200, OP), S7-200-internal CPU/CPU communication; transmission rates 9.6/19.2/187.5 kbit/s  Yes; As freely programmable interface with interrupt facility for serial data exchange with third-party devices with ASCII protocol transfer rates: 1.2 / 2.4 / 4.8 / 9.6 / 19.2 / 38.4 / 57.6 / 115.2 kbps; the PC/PPI cable can also be used as RS 232/RS 485 converter  19.2 kbit/s  19.2 kbit/s  19.2 kbit/s  6; High-speed counters (30 kHz each), 32 bit (incl. sign), can be used as up/down counters or for connecting 2 incremental encoders with 2 pulse trains offset by 90° (max. 20 kHz (A/B counters)); parameterizable enable and reset input; interrupt facilities (incl. call of subroutine with any content) when the setpoint is reached; reversal in counting direction, etc.  30 kHz  4; 4 rising edges and/or 4 falling edges  2; High-speed outputs, 20 kHz, with interrupt option; pulse-width and frequency modulation option
Interface type Protocols  • MPI  • PPI  • serial data exchange  MPI  • Transmission rate, min. • Transmission rate, max.  Integrated Functions  Counter  • Number of counters  • Counting frequency, max.  Number of alarm inputs  Number of pulse outputs  Limit frequency (pulse)	Yes; As MPI slave for data exchange with MPI masters (S7-300/S7-400 CPUs, OPs, TDs, Push Button Panels); S7-200-internal CPU/CPU communication is possible in the MPI network with restrictions; transmission rates: 19.2/187.5 kbit/s  Yes; with PPI protocol for program functions, HMI functions (TD 200, OP), S7-200-internal CPU/CPU communication; transmission rates 9.6/19.2/187.5 kbit/s  Yes; As freely programmable interface with interrupt facility for serial data exchange with third-party devices with ASCII protocol transfer rates: 1.2 / 2.4 / 4.8 / 9.6 / 19.2 / 38.4 / 57.6 / 115.2 kbps; the PC/PPI cable can also be used as RS 232/RS 485 converter  19.2 kbit/s  19.2 kbit/s  19.2 kbit/s  6; High-speed counters (30 kHz each), 32 bit (incl. sign), can be used as up/down counters or for connecting 2 incremental encoders with 2 pulse trains offset by 90° (max. 20 kHz (A/B counters)); parameterizable enable and reset input; interrupt facilities (incl. call of subroutine with any content) when the setpoint is reached; reversal in counting direction, etc.  30 kHz  4; 4 rising edges and/or 4 falling edges  2; High-speed outputs, 20 kHz, with interrupt option; pulse-width and frequency modulation option
Interface type Protocols  • MPI  • PPI  • serial data exchange  MPI  • Transmission rate, min. • Transmission rate, max.  Integrated Functions  Counter • Number of counters  • Counting frequency, max.  Number of alarm inputs  Number of pulse outputs  Limit frequency (pulse)  Potential separation  Potential separation digital inputs	Yes; As MPI slave for data exchange with MPI masters (S7-300/S7-400 CPUs, OPs, TDs, Push Button Panels); S7-200-internal CPU/CPU communication is possible in the MPI network with restrictions; transmission rates: 19.2/187.5 kbit/s  Yes; with PPI protocol for program functions, HMI functions (TD 200, OP), S7-200-internal CPU/CPU communication; transmission rates 9.6/19.2/187.5 kbit/s  Yes; As freely programmable interface with interrupt facility for serial data exchange with third-party devices with ASCII protocol transfer rates: 1.2 / 2.4 / 4.8 / 9.6 / 19.2 / 38.4 / 57.6 / 115.2 kbps; the PC/PPI cable can also be used as RS 232/RS 485 converter  19.2 kbit/s  187.5 kbit/s  6; High-speed counters (30 kHz each), 32 bit (incl. sign), can be used as up/down counters or for connecting 2 incremental encoders with 2 pulse trains offset by 90° (max. 20 kHz (A/B counters)); parameterizable enable and reset input; interrupt facilities (incl. call of subroutine with any content) when the setpoint is reached; reversal in counting direction, etc.  30 kHz  4; 4 rising edges and/or 4 falling edges  2; High-speed outputs, 20 kHz, with interrupt option; pulse-width and frequency modulation option  20 kHz

Detection assessment digital cut-ut-	
Potential separation digital outputs	V 0 1
between the channels	Yes; Optocoupler
between the channels, in groups of	5
Permissible potential difference	
between different circuits	500 V DC between 24 V DC and 5 V DC
Degree and class of protection	
IP degree of protection	IP20
Ambient conditions	
Ambient temperature during operation	
<ul> <li>horizontal installation, min.</li> </ul>	0°C
<ul> <li>horizontal installation, max.</li> </ul>	55 °C
<ul> <li>vertical installation, min.</li> </ul>	0°C
vertical installation, max.	45 °C
Air pressure acc. to IEC 60068-2-13	
<ul> <li>permissible range, lower limit</li> </ul>	860 hPa
permissible range, upper limit	1 080 hPa
Relative humidity	
<ul> <li>Operation, min.</li> </ul>	5 %
Operation, max.	95 %; RH class 2 in accordance with IEC 1131-2
configuration / header	
configuration / programming / header	
Command set	Bit logic instructions, compare instructions, timer instructions, counter instructions, clock instructions, transmissions instructions, table instructions, logic instructions, shift and rotate instructions, conversion instructions, program control instructions, interrupt and communications instructions, logic stack instructions, integer maths, floating-point math instructions, numerical functions
Program processing	free cycle (OB 1), interrupt-controller, time-controlled (1 to 255 ms)
Program organization	1 OB, 1 DB, 1 SDB subroutines with/without parameter transfer
<ul> <li>Number of subroutines, max.</li> </ul>	64
Programming language	
— LAD	Yes
— FBD	Yes
STL	Yes
Know-how protection	
<ul> <li>User program protection/password protection</li> </ul>	Yes; 3-stage password protection
connection method / header	
Plug-in I/O terminals	Yes
Dimensions	
Width	120.5 mm
Height	80 mm
Depth	62 mm
Weights	
Weight, approx.	360 g
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